

ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

**Title of
Invention**

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN
AN RF POWER AMPLIFIER

Submission Type : Information Disclosure
Statement

Application Number:

10/743221



EFS ID:

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First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

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File Listing:

| Doc. Name | File Name | Size (Bytes) | Date Produced (yyyymmdd) |
|------------------|---------------------|--------------|--------------------------------|
| us-ids | SILP63-1-usidst.xml | 1477 | 2005-02-13 |
| us-ids | us-ids.dtd | 7763 | 2005-02-13 |
| us-ids | us-ids.xsl | 12026 | 2005-02-13 |
| package-data | SILP63-1-pkda.xml | 1766 | 2005-02-13 |
| package-data | package-data.dtd | 27025 | 2005-02-13 |
| package-data | us-package-data.xsl | 19263 | 2005-02-13 |
| Total files size | | 69320 | |

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